HONGZHI ELECTRONIC LTD.

NO.6, PUJIANG ROAD, SHANTOU, GUANGDONG

REPORT NO.201001150002

ENGINEERING REPORT

Customer Part No	HEL Par	t No. or S	Sample N	IoY1-	12-2F4-1I	D-472M	
Customer							
1	00V-472M-F-12	2MM		Lot No.			
Date of Inspection:	2010-1-13			_Inspection	size:	20	pcs
Room-Temperature:2	25°C	R.H	_70	% Te	st Frequenc	y:1K	HZ

Elect	rical propert	ies:	Mechanical properties:			
NO.	Cap.(PF)	D.F. Dissipation Factor	I.Rmin (MΩ)	T.V.	Item.	Measurements
1	4.486	0.003	10 ⁴	AC4KV	Diameter	Max.13mm
2	4411	0.0031	10 ⁴			
3	4338	0.0032	10 ⁴		Thickness	Max.5.5mm
4	4477	0.0032	10 ⁴			
5	4364	0.0033	10 ⁴		Lead Length	23mm+/-1mm
6	4706	0.003	10 ⁴			
7	4427	0.0031	10 ⁴		Lead Space	10mm+/-1mm
8	4631	0.0031	10 ⁴			
9	4520	0.0031	10 ⁴		Marking:	
10	4552	0.0031	10 ⁴			
11	4334	0.0030	10 ⁴			
12	4555	0.0029	10 ⁴			222M
13	4538	0.0029	10 ⁴			ZZZIVI
14	4391	0.0029	10 ⁴			coc 🌃
15	4378	0.0123	10 ⁴		\ (r	Ja PS /
16	4912	0.0030	10 ⁴			/
17	4391	0.0030	10 ⁴			400V-Y1
18	4689	0.0030	10 ⁴			
19	4552	0.0032	10 ⁴			
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 10^{4}

Measurement Instrument

0.0031

Cap.D.F.: HP 4278A

4505

20

I.R.: Chen Hwa Clc-202A

T.V.:W-CDF-4

Safety Recognized Ceramic Capacitors

Part Numbering

Y1-12-2F4-1D-472M

☐ Rated Voltage/Safety Standard Recognized Type

Code	Rated voltage
Y1	AC400V
Y2	AC250V

 \Box Diameter of the disc: 5=5mm

☐ Temperature characteristics

Code	Temperature	Cap.Change or	
	characteristics	Temp.Coeff.	
2E4	Z5U	+20%-56%	
2F4	Z5V	+22%-82%	

☐ Lead space: 1E=7.5MM; 1D=10MM

☐ Capacitance:

Expressed by three figures. The unit is pico-farad (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers. 102PF=0.001UF

☐ Capacitance Tolerance

Code	Capacitance
	Tolerance
J	+/-5%
K	+/-10%
M	+/ - 20%
Z	+80%,-20%

Safety Recognized Ceramic Capacitors

Specification and Test Methods

NO.	Item		Specification	Testing Method
1	Appearance Dimension	and	No marked defect on appearance form and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect. Dimensions should be measured with vernier caliper.
2	Marking		To be easily legible	The capacitor should be visually inspected.
3	Voltage proof	Between lead wires	No failure	Applied Voltage: Y1:4000VAC Y2:250VAC When applied 50/60Hz, use the Autotransformer to applied the specified voltage for 60s. Increase the test voltage from 0 to specified voltage vaule on the rate of 150V/s. Charge /Discharge current: Y1:below 20mA Y2:below 50mA
		Between lead wires and Body	No failure	First, the terminals of the capacitor should be connected together. A metal foil should be closely wrapped around the body of the capacitor to the distance of about 1mm/1KV of test voltage from each terminal. The test voltage should be applied between the body of the capacitor and terminals.
4	Insulation Resistance (I.R.)		10000MΩ min.	The insulation resistance should be measured with rated voltage within 60+/-5sec. of charging.
5	Capacitance		Within specified tolerance J:+/-5%; K:+/-10% M:+/-20%; Z:+/-80/20%	Test frequency: 1+/-0.2KHZ Test Voltage: 1.0V+/-0.2VRMS
6	Dissipation Factor (D.F.)		≤0.05	As above

7	Temperature	Y5P-B	Temperature ch	naracteristic guarantee is -25 to
	Characteristics	Y5U-E Y5V-F	+80°C	
			Char.	Capacitance
				Change
			В	Within+/- 10%
			Е	Within
				+20%-20%
			F	Within
				+56%-20%

Safety Recognized Ceramic Capacitors

Specification and Test Methods

NO.	Item		Specification	Testing Method
8	Robustness of terminals	Bending	Lead wire should not be cut off, capacitor should not be broken.	Each lead wire should be subjected a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90°bend in the opposite direction a the rate of one bend in 2 to 3 sec. Applied a tensile weight gradually to each lead wire in the radial direction of the capcitor up to 0.5N and keep it for 10+/-1 sec.
10	Solderability of Leads Soldering		Lead wire should be soldered with uniform coating on the axial direction over 60% of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder of 270+/-5°C for 2+/-0.5sec.
	Effect	Appearance	No marked defect	The lead wires should be immersed in solder of 270+/-5°C for 3.0+/-0.5 sec.
		Capacitance	Within specified tolerance	The inspect samples should be stored for

	Change		24+/-2 hours at room condition.
	Voltage	Short Circuit Phenomenon	
	proof	was not occurred.	