TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP181

Office Machine

Programmable Controllers

AC Adapter

I/O Interface Board

The TOSHIBA mini flat coupler TLP181 is a small outline coupler, suitable for surface mount assembly.

TLP181 consist of a photo transistor optically coupled to a gallium arsenide infrared emitting diode. Since TLP181 is smaller than DIP package, it's suitable for high-density surface mounting applications such as programmable controllers

Collector-emitter voltage: 80V (min)

• Current transfer ratio: 50% (min)

Rank GB: 100% (min)

• Isolation voltage: 3750Vrms (min)

• Operation Temperature:-55 to 110 °C

Safety Standards

UL recognized: UL1577, File No. E67349

cUL recognized: CSA Component Acceptance Service No. 5A

File No.E67349

BSI approved: BS EN60065:2002, certificate No.8285

BS EN60950-1:2006 certificate No.8286

Option (V4) type

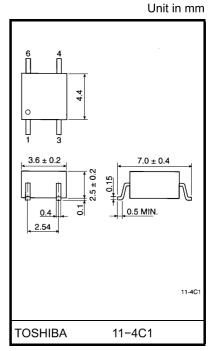
VDE approved: EN60747-5-2

Maximum Operating Insuration Voltage: 565 Vpk

Highest Permissible Overvoltage: 6000 Vpk

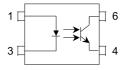
(Note): When a EN60747-5-2 approved type is needed,

Please designate "Option(V4)"



Weight: 0.09 g (Typ.)

Pin Configuration (top view)



- 1: Anode
- 3: Cathode
- 4: Emitter
- 6: Collector

Current Transfer Ratio

Tuno	Classification	(I _C	sfer Ratio (%) / I _F) = 5V, Ta = 25°C	Manking Of Classification		
Туре	*1	Min	Max	Marking Of Classification		
	Blank	50	600	Blank ,Y,Y■,YE,G,G■,GR,B, B■,BL,GB		
	Rank Y	50	150	YE		
	Rank GR	100	300	GR		
	Rank BL	200	600	BL		
TLP181	Rank GB	100	600	GB		
	Rank YH	75	150	Y=		
	Rank GRL	100	200	G		
	Rank GRH	150	300	G■		
	Rank BLL	200	400	В		

^{*1:} EX, Rank GB: TLP181 (GB)

⁽Note) Application, type name for certification test, please use standard product type name, i, e. TLP181 (GB): TLP181

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
	Forward current	l _F	50	mA
	Forward current detating	ΔI _F / °C	-1.4 (Ta ≥89°C)	mA / °C
LED	Pulse forward current (100µs pulse, 100pps)	I _{FP}	1	А
	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Collector-emitter voltage	V_{CEO}	80	V
	Emitter-collector voltage	V _{ECO}	7	V
5	Collector current	lc	50	mA
Detecto	Collector power dissipation (1 Circuit)	PC	150	mW
	Collector power dissipation derating (1 Circuit Ta ≥ 25°C)	ΔP _C / °C	-1.5	mW / °C
	Junction temperature	Tj	125	°C
Stor	age temperature range	T _{stg}	–55 to 125	°C
Оре	erating temperature range	T _{opr}	–55 to 110	°C
Lea	d soldering temperature	T _{sol}	260 (10s)	°C
Total package power dissipation		PT	200	mW
Total package power dissipation derating (Ta ≥ 25°C)		ΔP _T / °C	-2.0	mW / °C
Isolation voltage (AC, 1min., R.H. ≤ 60%) (Note 1)		BVS	3750	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two-terminal device: Pin1, 3 shorted together and pins 4, 6 shorted together

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{CC}	_	5	48	V
Forward current	lF	_	16	20	mA
Collector current	lc	_	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
	Collector–emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	80	_	_	V
J.	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	_	_	V
Detector	Collector dark current I _{CEO}	laza	V _{CE} = 48 V, (Ambient light below 1000 lx)	_	0.01 (2)	0.1 (10)	μΑ
		ICEO	V _{CE} = 48 V, Ta = 85°C, (Ambient light below 1000 lx)	_	2 (4)	50 (50)	μΑ
	Capacitance (collector to emitter)	C _{CE}	V = 0, f = 1 MHz	_	10	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	MIn	Тур.	Max	Unit
Current transfer ratio	1- /1-	I _F = 5 mA, V _{CE} = 5 V Rank GB	50	_	600	%
Current transfer fatto	IC / IF		100		600	
Saturated CTR	I _C / I _F (sat)	IF = 1 mA, V _{CE} = 0.4 V Rank GB		60		%
Saturated CTIX			30	_	_	
	VCE (sat)	I _C = 2.4 mA, I _F = 8 mA	_	_	0.4	
Collector–emitter saturation voltage		I_C = 0.2 mA, I_F = 1 mA Rank GB	_	0.2	_	V
			_	_	0.4	
Off-state collector current	I _{C (off)}	V _F = 0.7V, V _{CE} = 48 V		1	10	μΑ

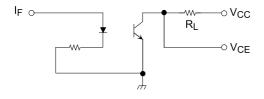
Isolation Characteristics (Ta = 25°C)

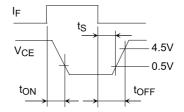
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	CS	V _S = 0V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	3750	_	_	- V _{rms}
Isolation voltage		AC, 1 second, in oil		10000	_	
		DC, 1 minute, in oil	_	10000		V _{dc}

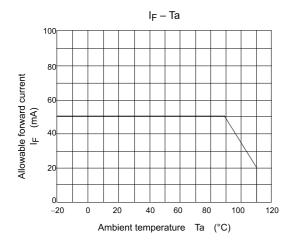
Switching Characteristics (Ta = 25°C)

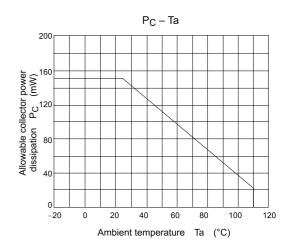
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t _r		_	2	_	- µs
Fall time	t _f	$V_{CC} = 10 \text{ V, I}_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$	_	3	_	
Turn-on time	t _{on}	$R_L = 100\Omega$	_	3	_	
Turn-off time	t _{off}		_	3	_	
Turn-on time	ton		_	2	_	
Storage time	t _s	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$	_	25	_	μs
Turn-off time	t _{OFF}		_	40	_	

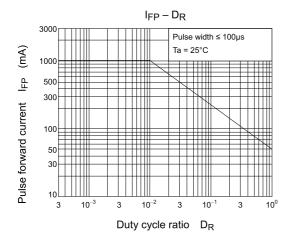
Fig. 1 Switching time test circuit

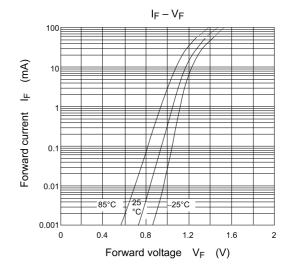


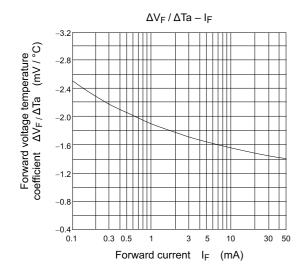


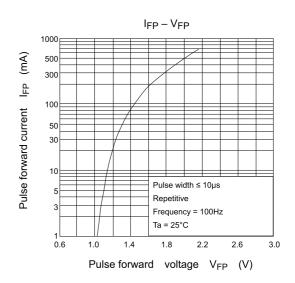


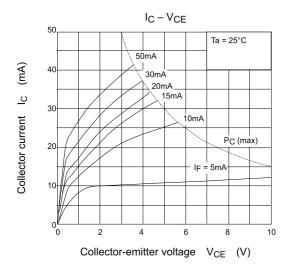


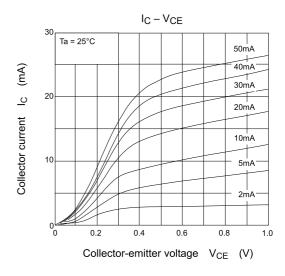


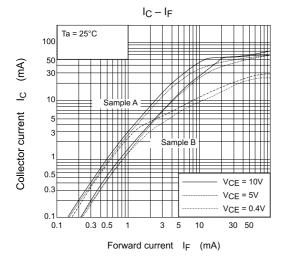


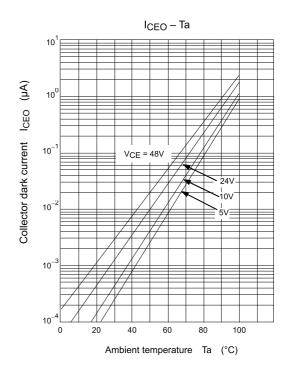


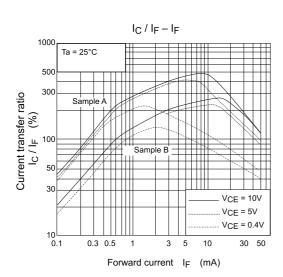


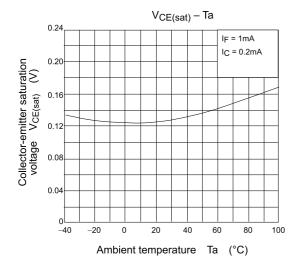


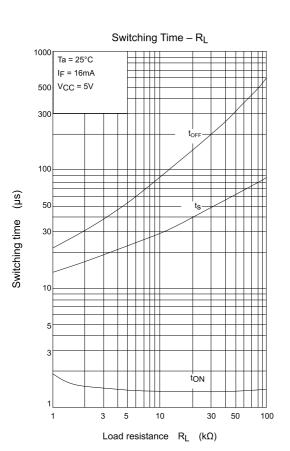


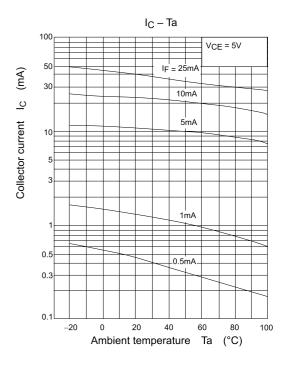


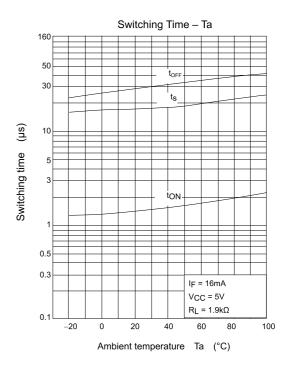












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